

application NOTE



Using Sedona 1.2 Components from Tridium's Kits

Introduction

This application note assists in the understanding of the Sedona components provided in Tridium's Sedona-1.2.28 release. Some of the Sedona components were changed or added since the previous release. New with the 1.2 release is that the Sedona components, previously concentrated in one Control kit, are now organized in smaller kits under a functional name. Components discussed in this document can be found in the following kits:

- basicSchedule
- datetimeSTD
- func
- hvac
- logic
- math
- pricom
- timing
- types

The intent of this document is to explain the potential use of those components supplied by Tridium in their Sedona 1.2 release. All are included in Contemporary Controls' BASremote and BAScontrol product families. They have not been modified for use in these products. Contemporary Controls has product specific Sedona kits that address the uniqueness of the IO structure in the BASremote and BAScontrol products. These kits are not mentioned in this document. It is Contemporary Controls' policy to provide all Sedona kits to the Sedona Framework community without charge or license. This includes kits obtained from Tridium, kits with modified Tridium components, kits developed solely by Contemporary Controls to improve the control options available to systems integrators, and kits specific to Contemporary Controls' hardware. Any feedback is welcomed.

Application Note — Using Sedona 1.2 Components

Variable Types

Although there are several variable types used by Sedona, three are the most interesting — Boolean, Float and Integer. You can define constants for each type and use converting components to change the data representation to a different type.

ConstBo

types::ConstBool

Outfalse

ConstFl

types::ConstFloat

Out0.00

ConstIn

types::ConstInt

Out0

Notice the format of the component output:

Boolean has true/false

Floats have a decimal point

Integers have no decimal point

These are constant components that can be preset. However, they must be saved or the settings will be lost.

Configuring Constants

ConstBo

types::ConstBool

Out

ConstFl

types::ConstFloat

Out

ConstIn

types::ConstInt

Out

Views

Actions

CutCtrl+X

CopyCtrl+C

PasteCtrl+V

Paste Special

DuplicateCtrl+D

DeleteDelete

Link Mark

Link From

Link To

RenameCtrl+R

Reorder

Export

Pin Slots

Set True

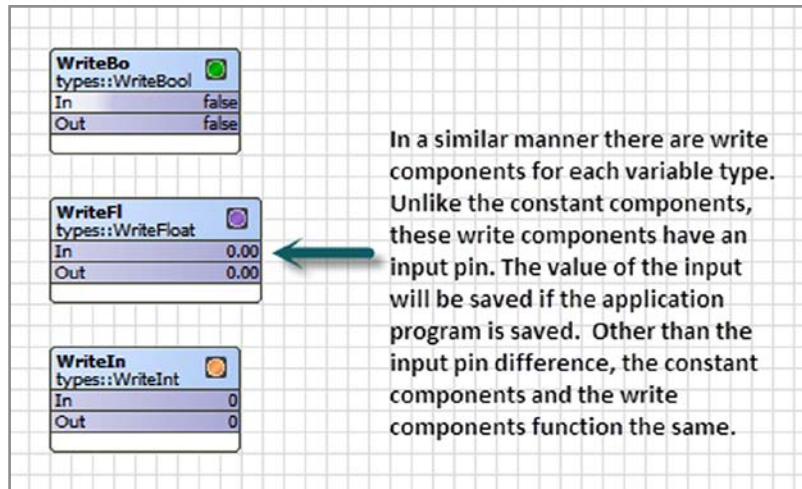
Set False

Set Null

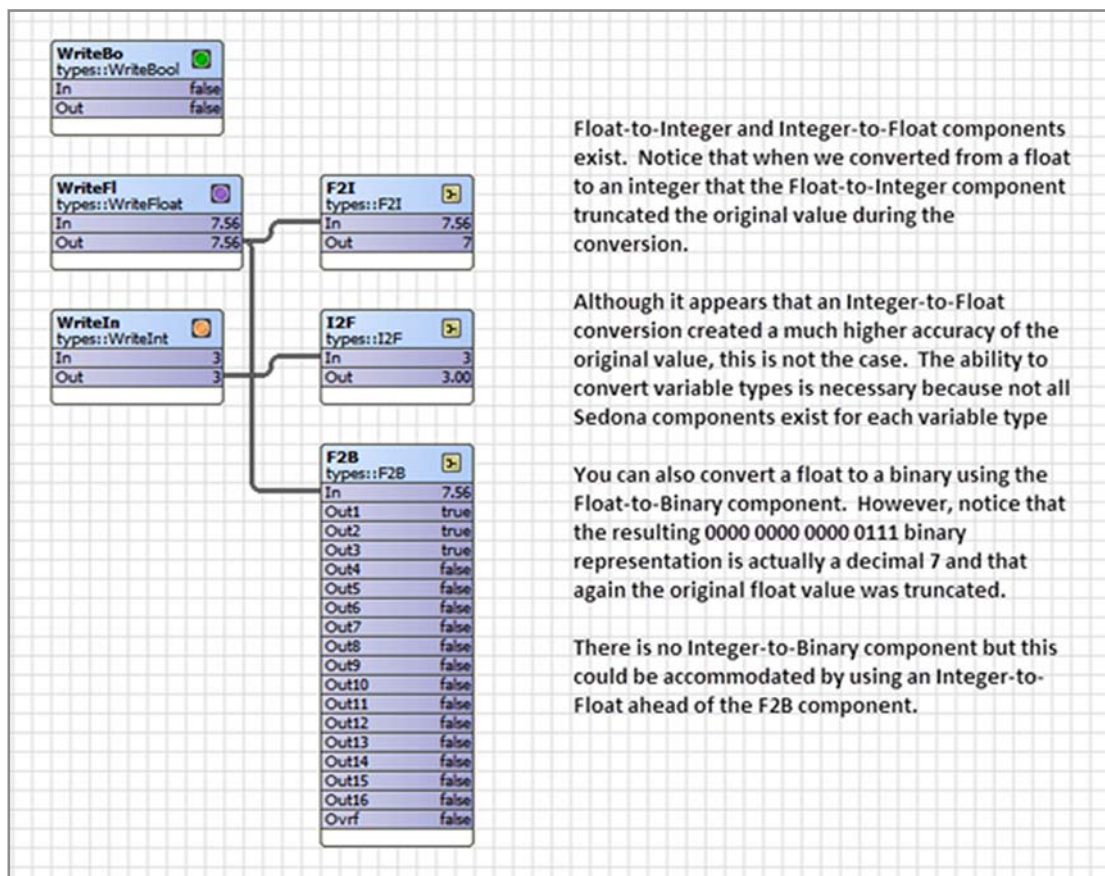
You can set the value of the constant by right-clicking on the component and then selecting Actions. For the ConstBo component your choices are True, False or Null. Null is seldom used.

Application Note — Using Sedona 1.2 Components

Using Write Components

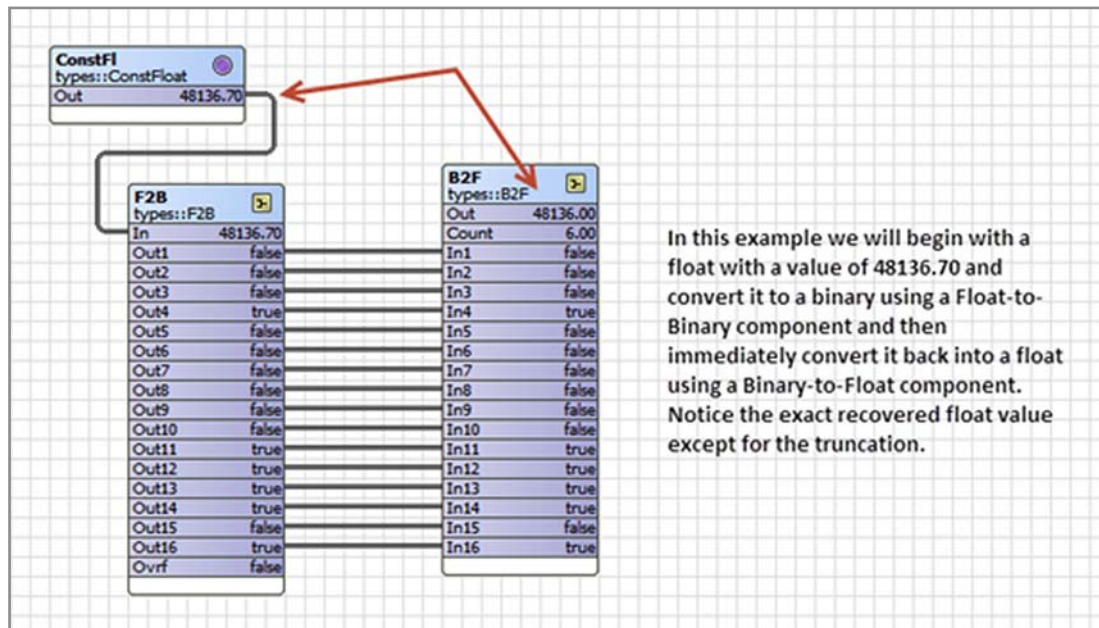


Converting Between Component Types

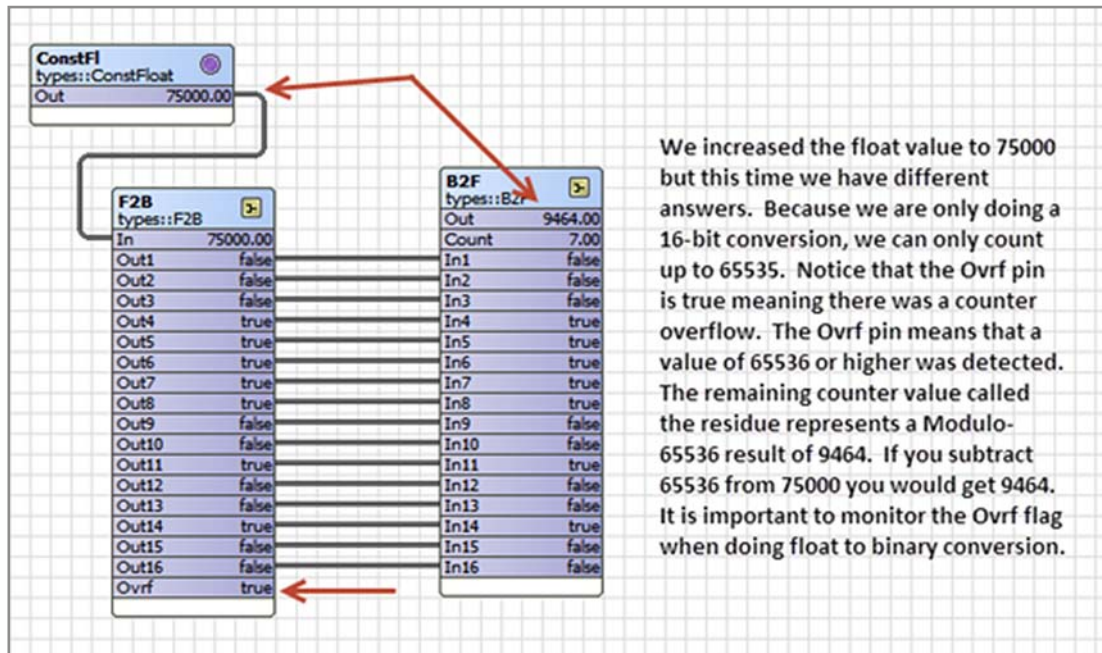


Application Note — Using Sedona 1.2 Components

Float-to-Boolean and Boolean-to-Float Conversion



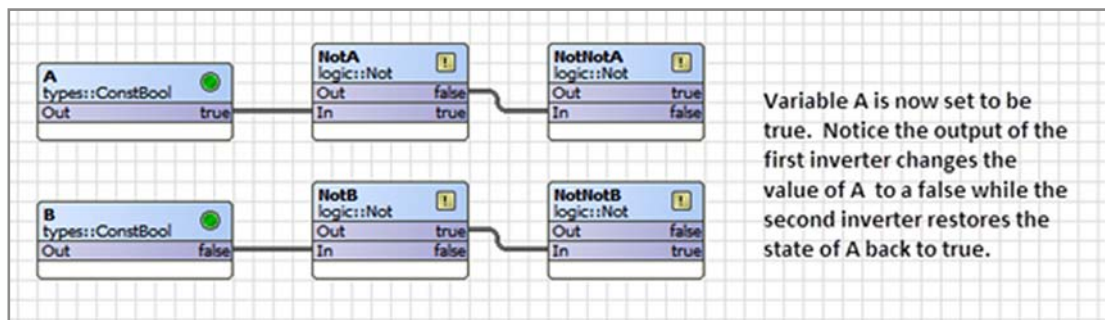
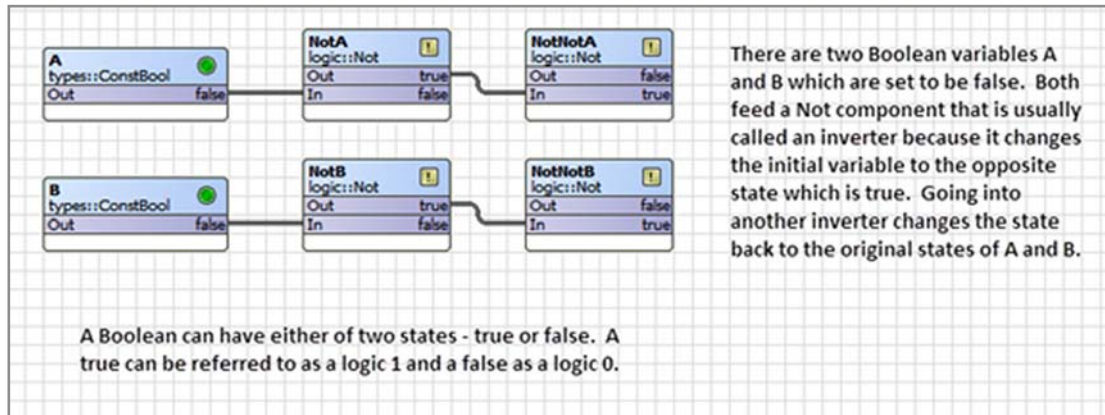
In this example we will begin with a float with a value of 48136.70 and convert it to a binary using a Float-to-Binary component and then immediately convert it back into a float using a Binary-to-Float component. Notice the exact recovered float value except for the truncation.



We increased the float value to 75000 but this time we have different answers. Because we are only doing a 16-bit conversion, we can only count up to 65535. Notice that the **Ovrf** pin is true meaning there was a counter overflow. The **Ovrf** pin means that a value of 65536 or higher was detected. The remaining counter value called the residue represents a Modulo-65536 result of 9464. If you subtract 65536 from 75000 you would get 9464. It is important to monitor the **Ovrf** flag when doing float to binary conversion.

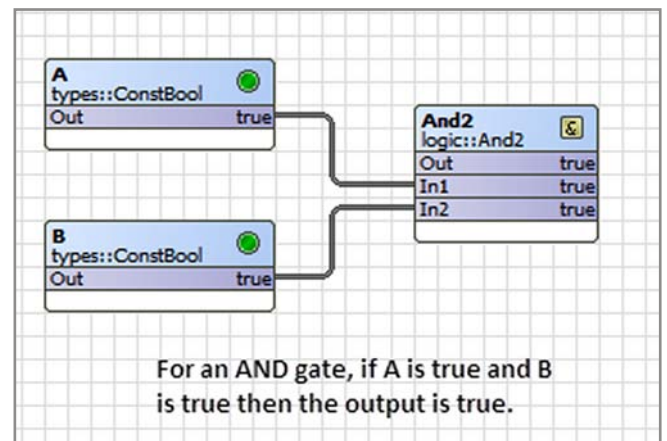
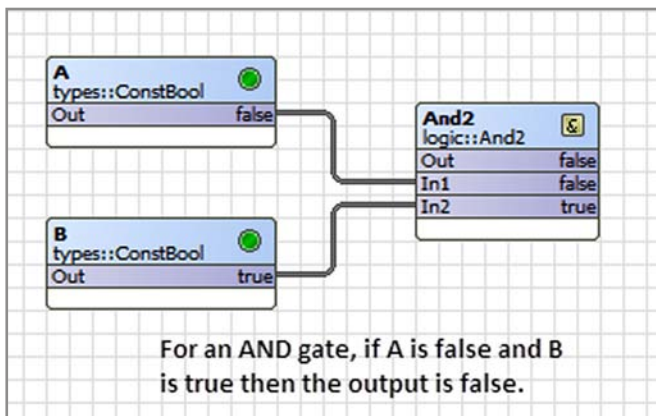
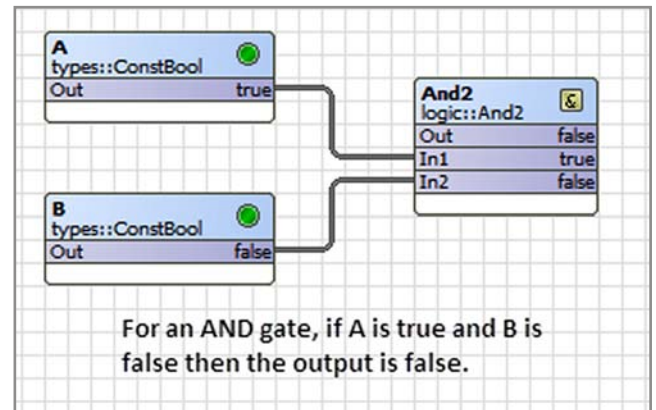
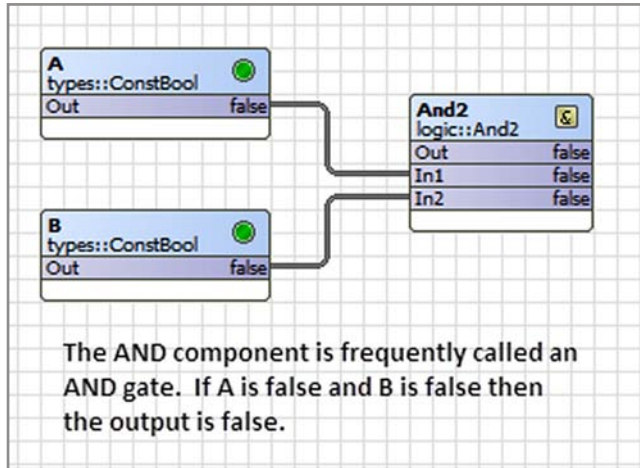
Application Note — Using Sedona 1.2 Components

Negating a Boolean Variable — Inverting Your Logic



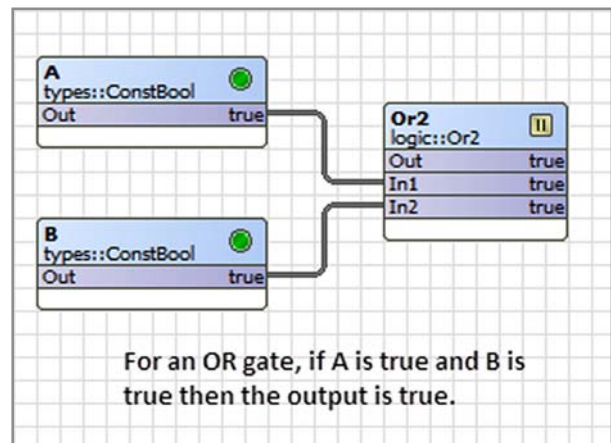
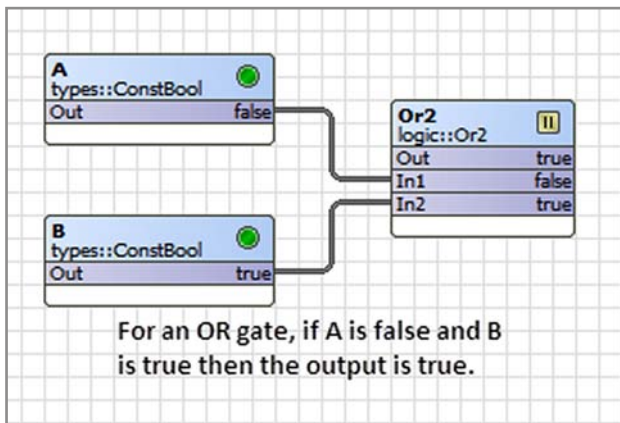
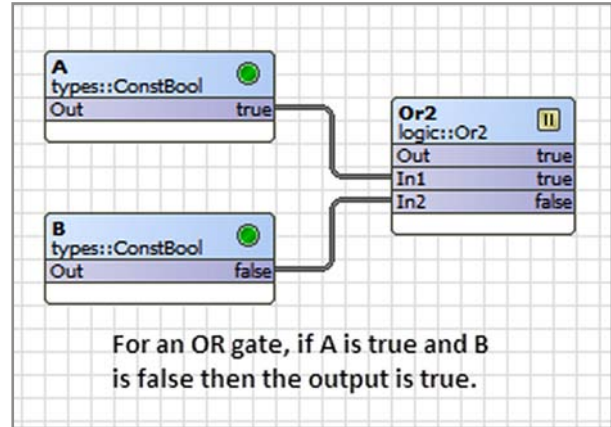
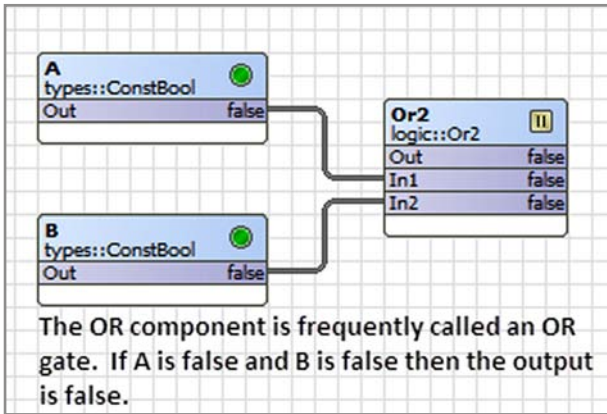
Application Note — Using Sedona 1.2 Components

Boolean Product — “ANDing” Boolean Variables



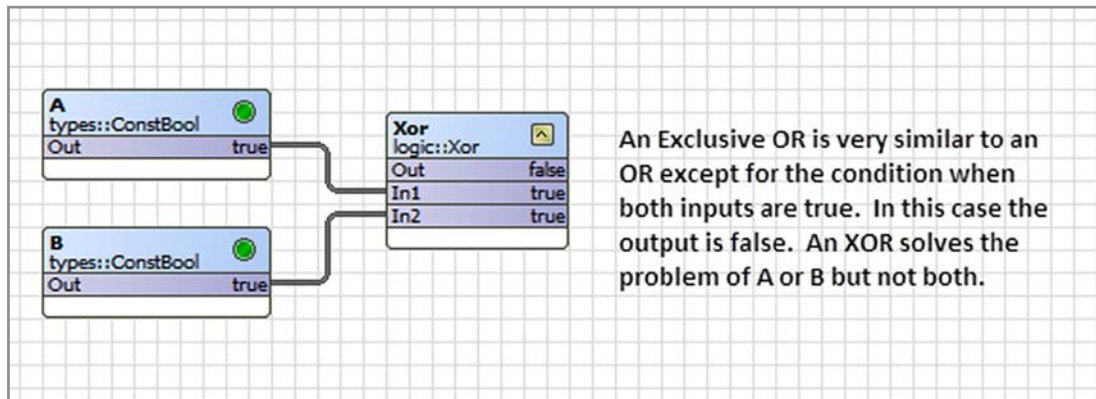
Application Note — Using Sedona 1.2 Components

Boolean Sum — “Oring” Boolean Variables

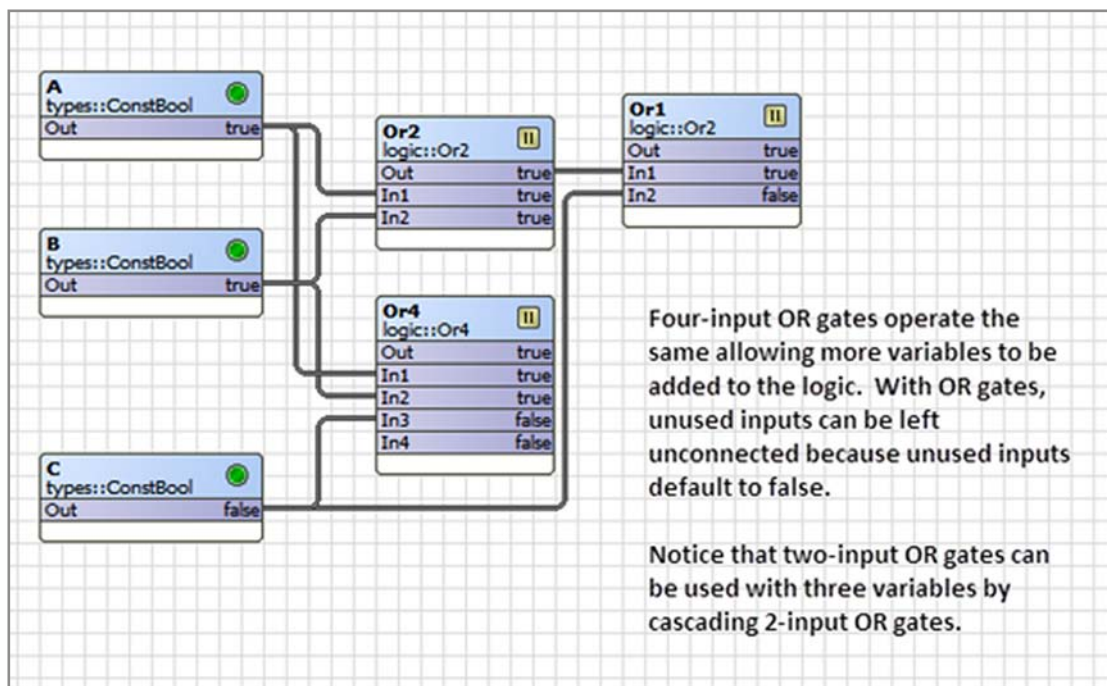


Application Note — Using Sedona 1.2 Components

Exclusive OR — A OR B but Not Both

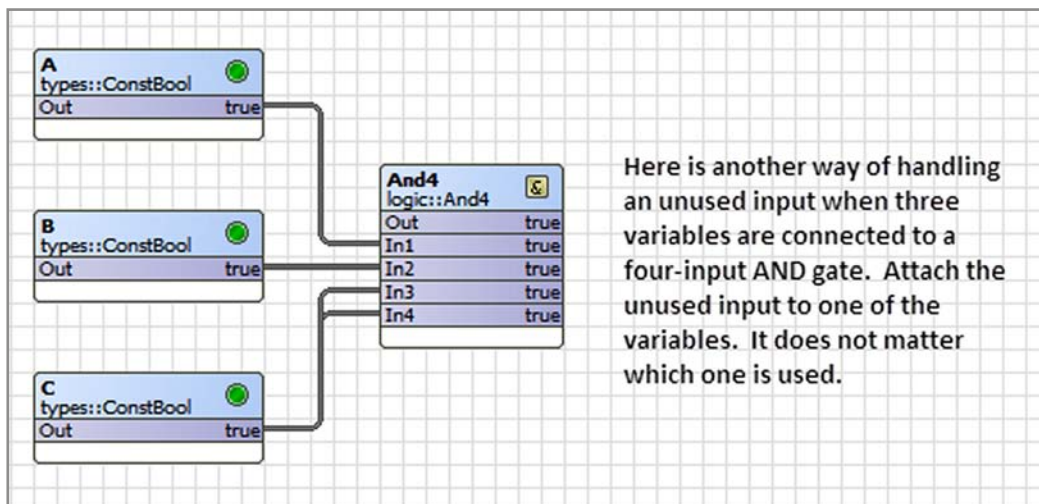
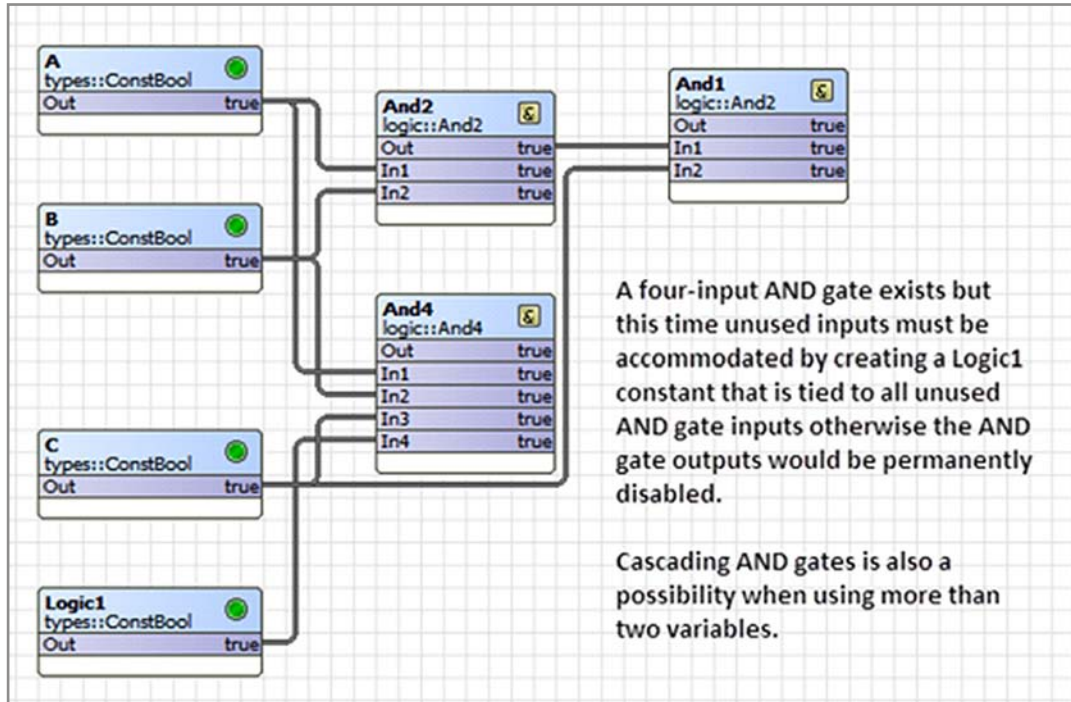


Cascading Logic Blocks and Unused Inputs



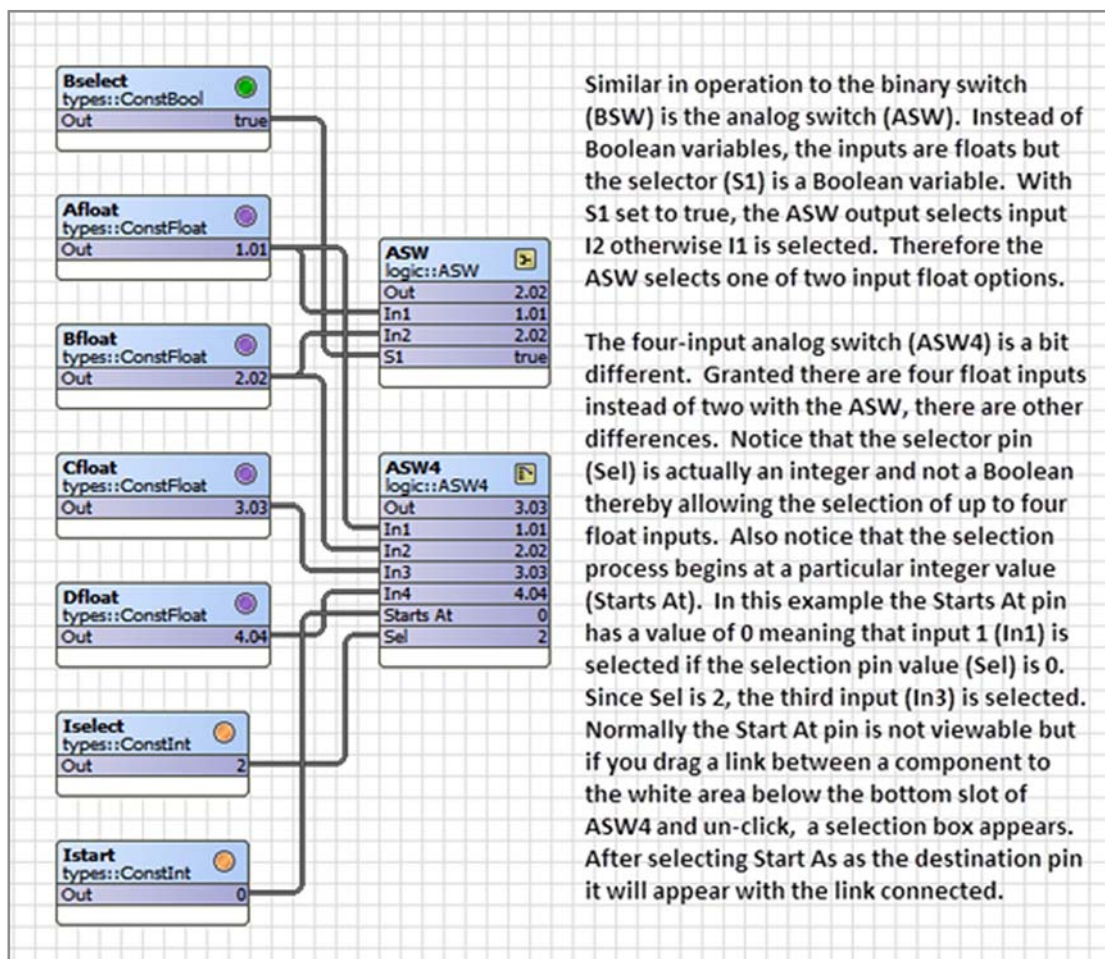
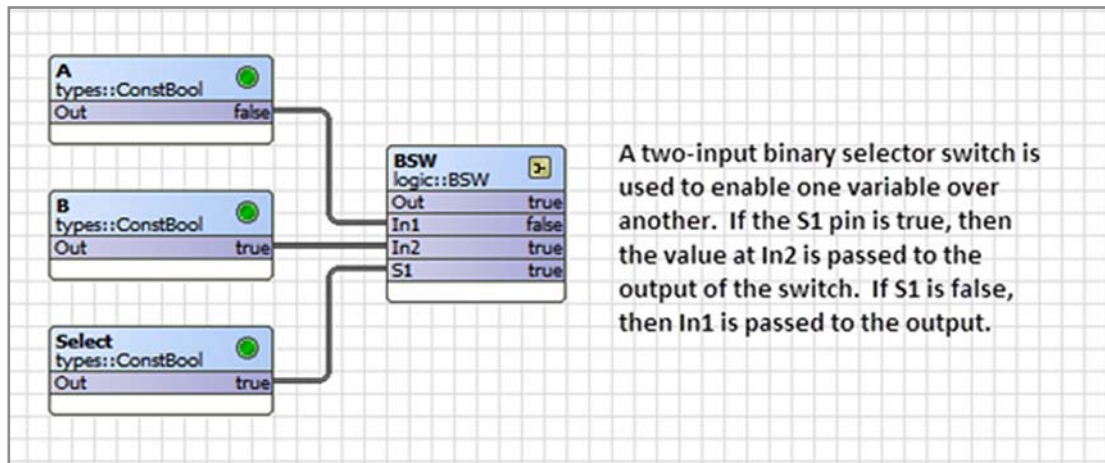
Application Note — Using Sedona 1.2 Components

Cascading Logic Blocks and Unused Inputs (continued)



Application Note — Using Sedona 1.2 Components

Boolean, Float or Integer Selection



Application Note — Using Sedona 1.2 Components

Boolean, Float or Integer Selection (continued)

Link

Istart [Source]

Meta

Out

Set

ASW4 [Target]

Meta

Out

In1

In2

In3

In4

Starts At

Sel

Link Istart.out -> ASW4.startsAt

OK

Cancel

types::ConstInt

Out

0

This is the dialog screen you will see when you need to add a link to a pin that is hidden. Select the source and destination pins and click OK and the link will appear.

Aint

types::ConstInt

Out

5

Bint

types::ConstInt

Out

4

Bselect

types::ConstBool

Out

true

ISW

logic::ISW

Out

4

In1

5

In2

4

S1

true

The integer switch (ISW) is very much like the BSW and the ASW. The selector is a Boolean but the inputs are integers. The output remains an integer. The logic is the same. If the selector (S1) is true, then the output follows In2 otherwise I1.

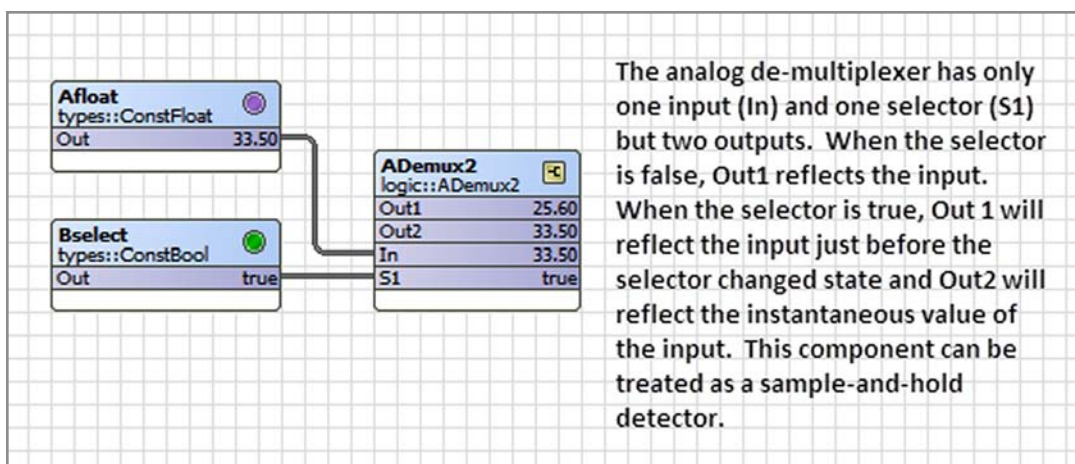
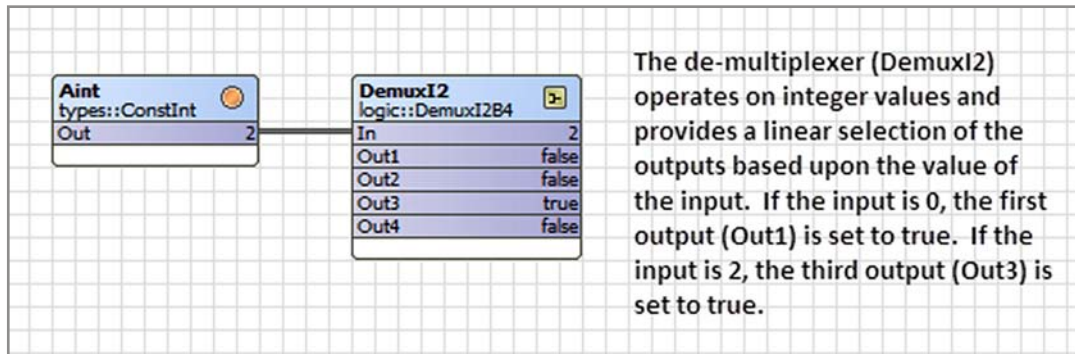
AN-SEDONA01-BA1

Page 11

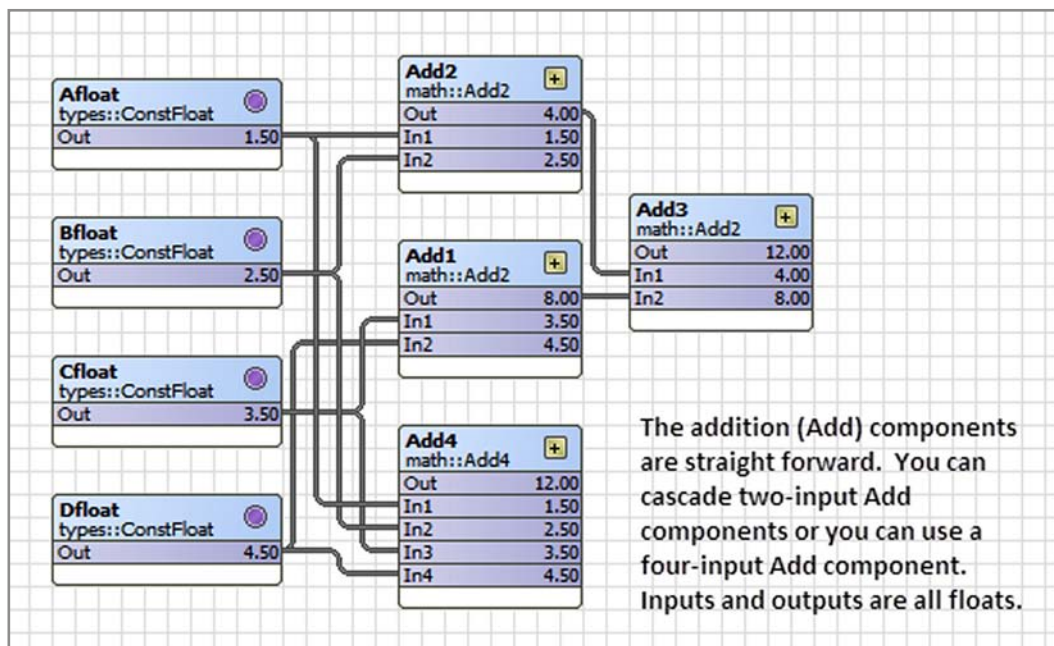
CONTEMPORARY CONTROLS

Application Note — Using Sedona 1.2 Components

De-Multiplexing

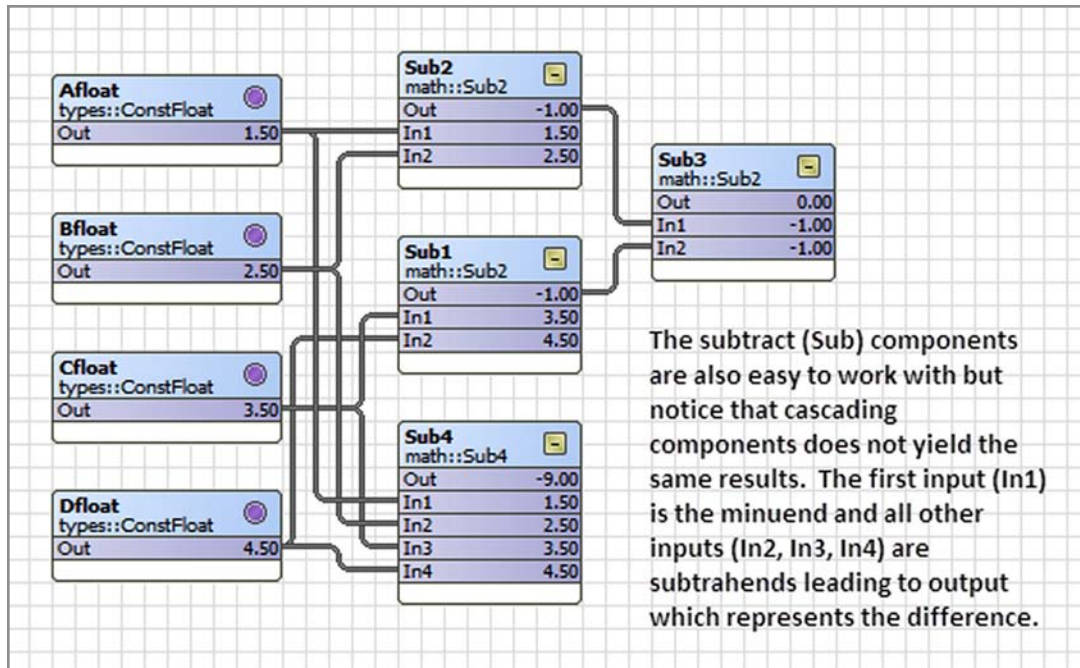


Float Addition

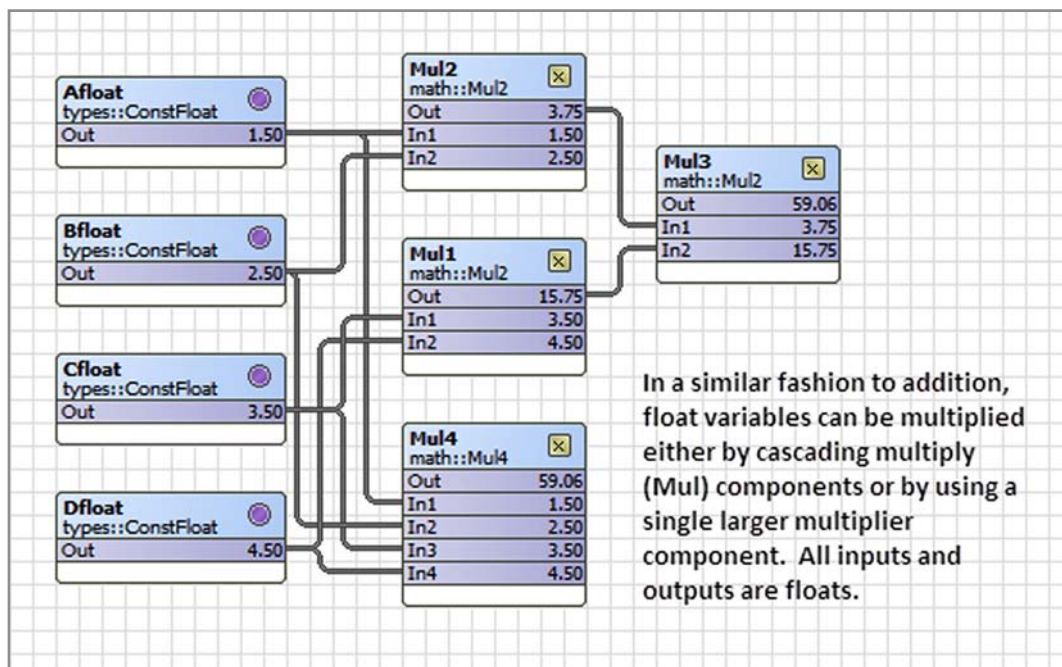


Application Note — Using Sedona 1.2 Components

Float Subtraction

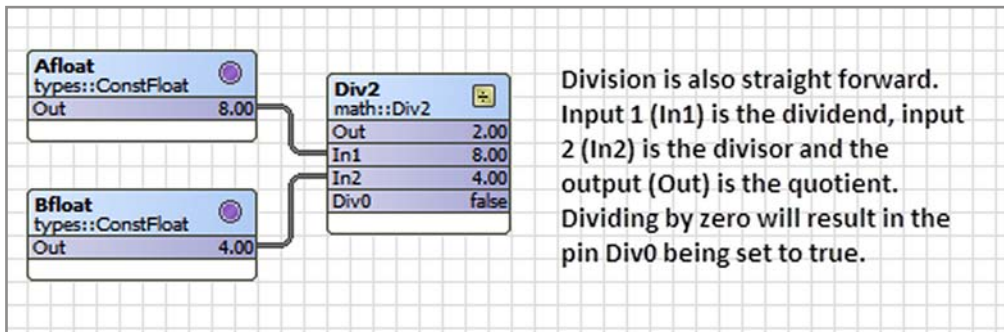


Float Multiplication

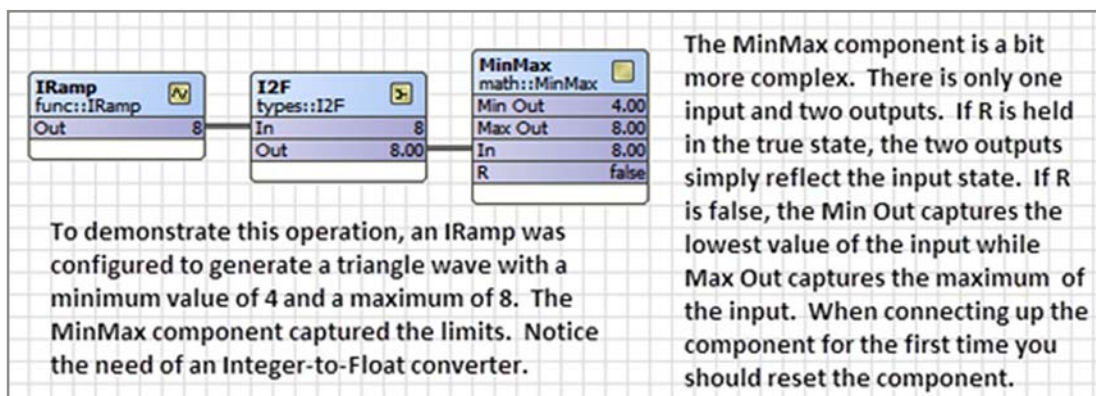
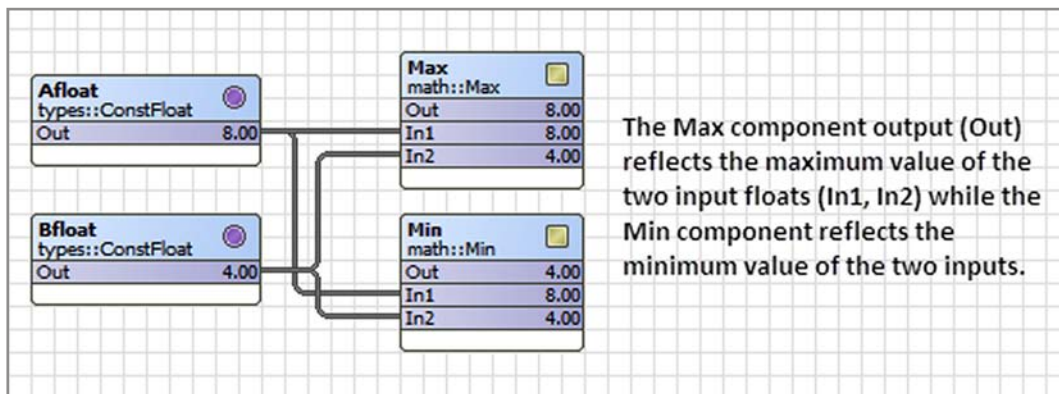


Application Note — Using Sedona 1.2 Components

Float Division

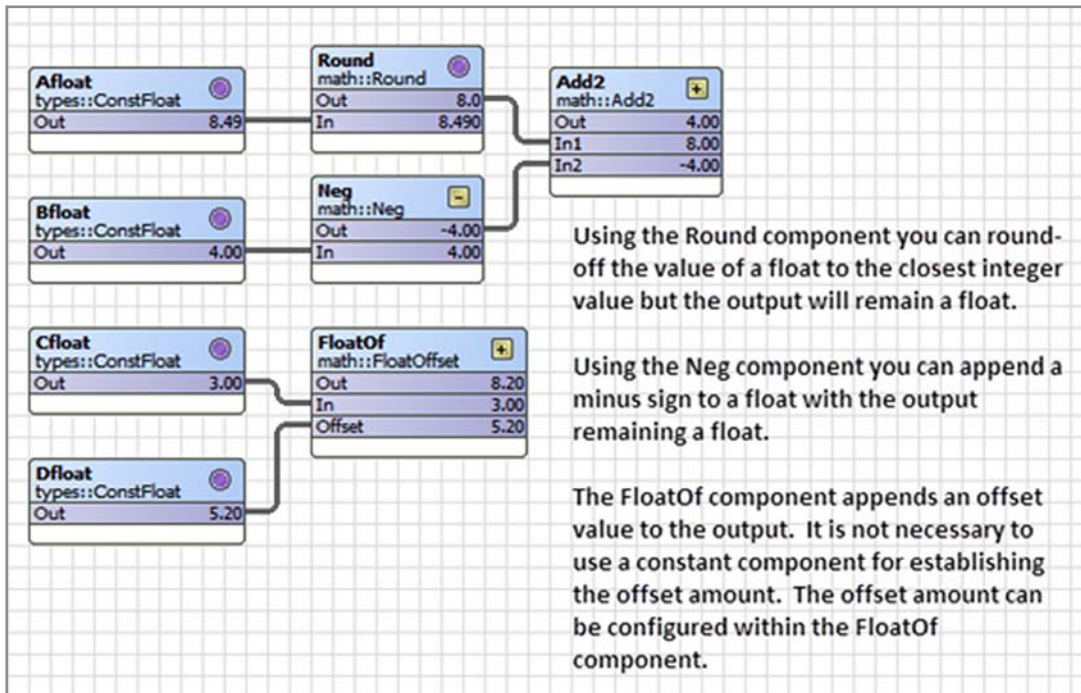


Finding Minimums and Maximums

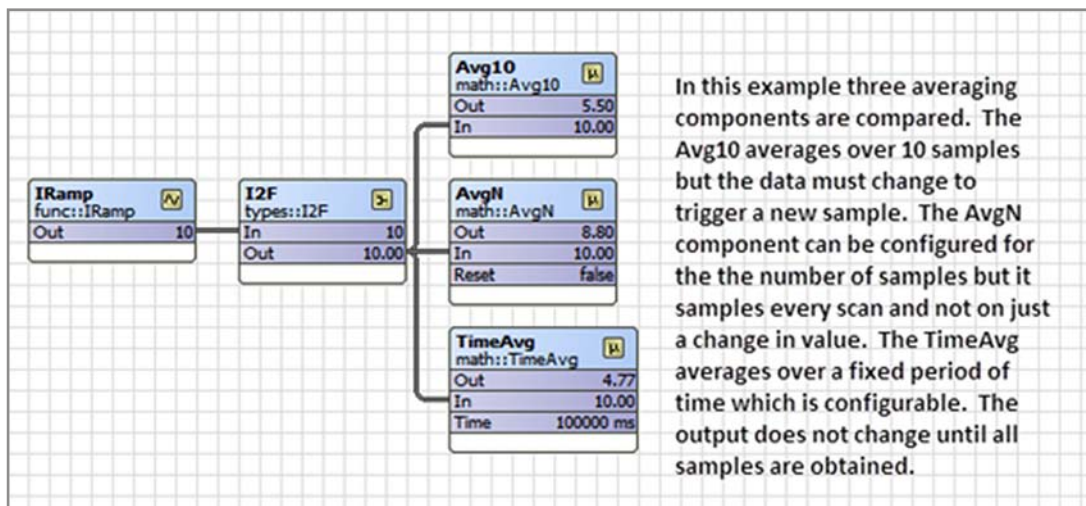
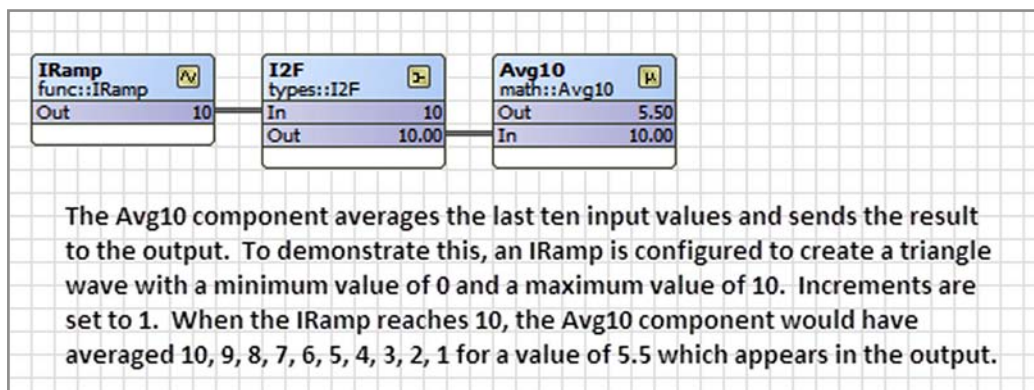


Application Note — Using Sedona 1.2 Components

Rounding Off Floats

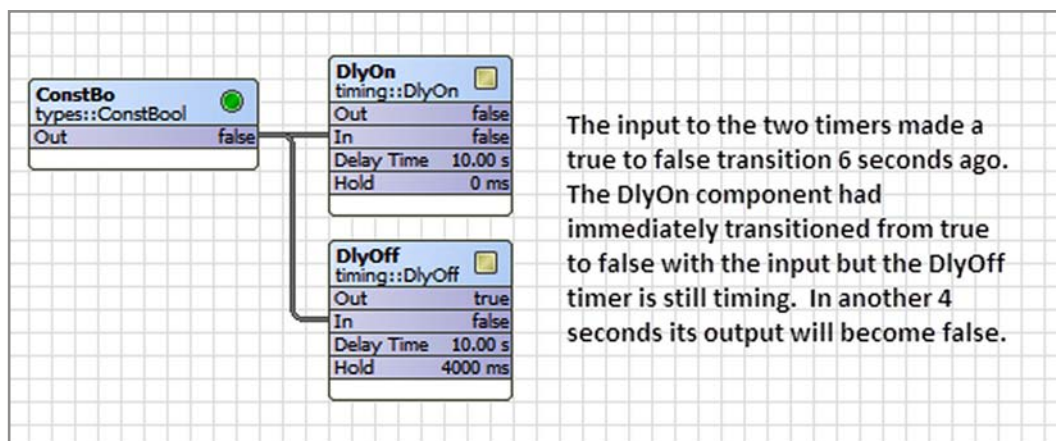
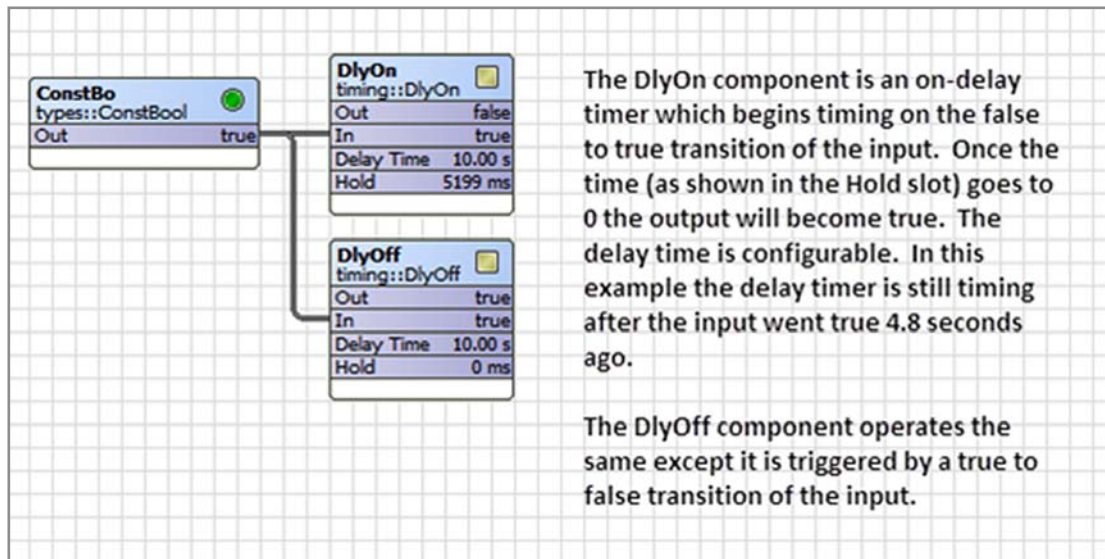


Averaging Successive Readings

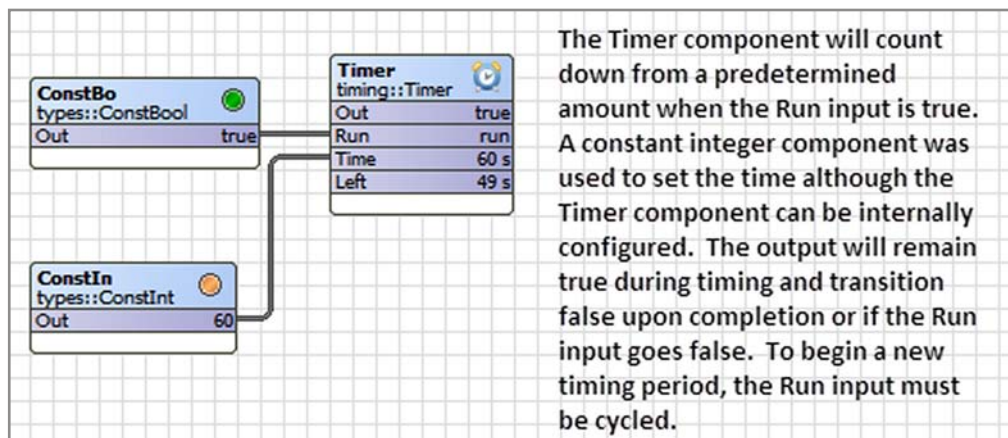


Application Note — Using Sedona 1.2 Components

On-Delays and Off-Delays

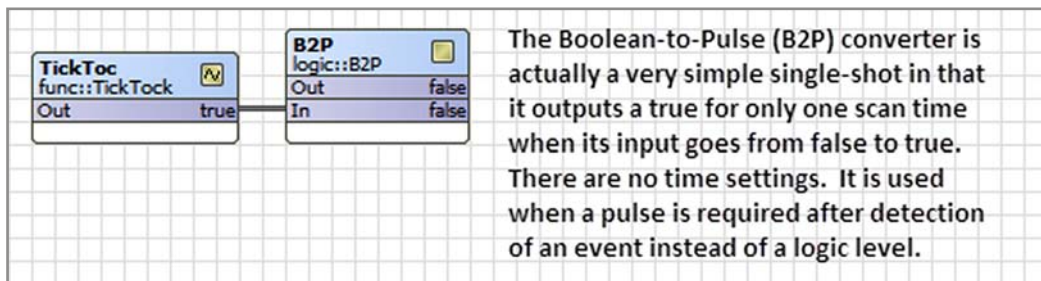
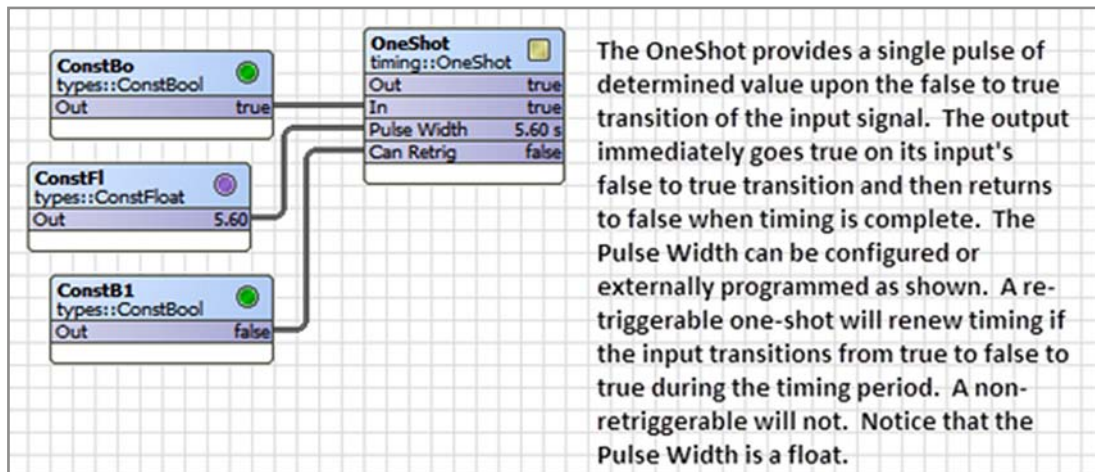


Using the Timer

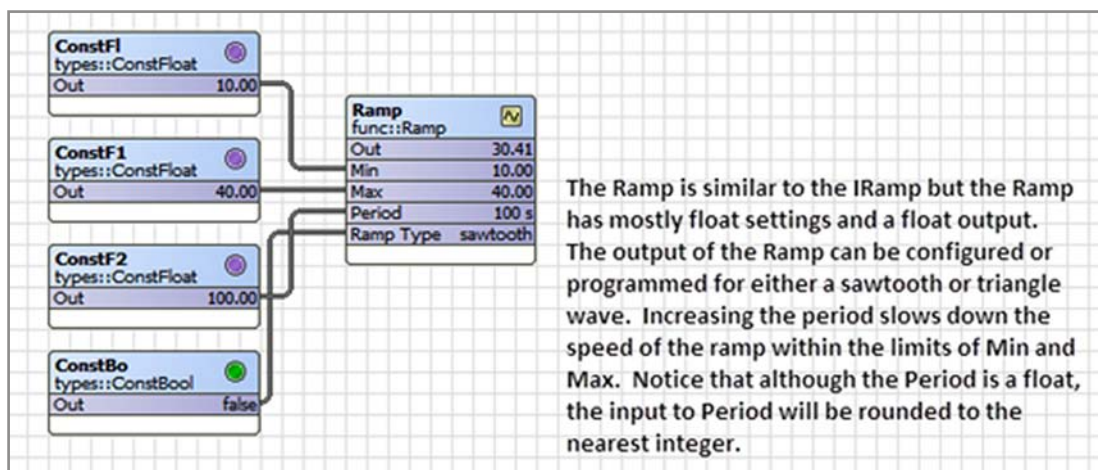
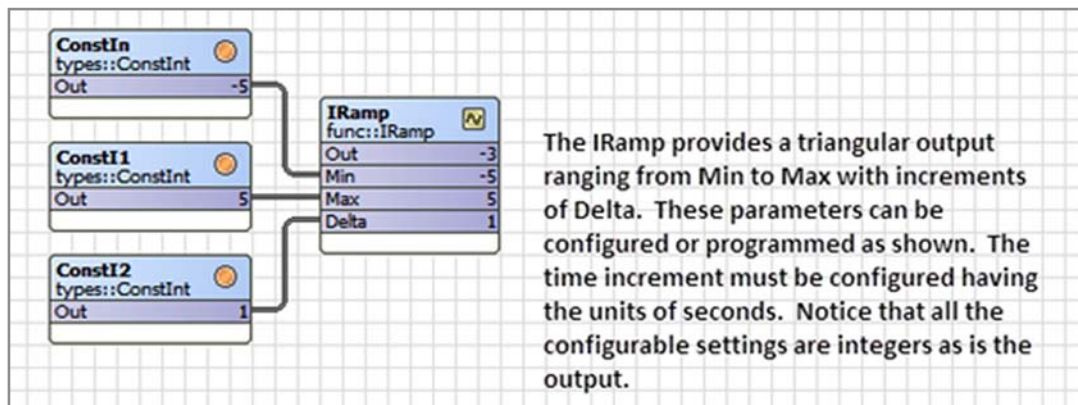


Application Note — Using Sedona 1.2 Components

Using One-Shots — Mono-Stable Multivibrators

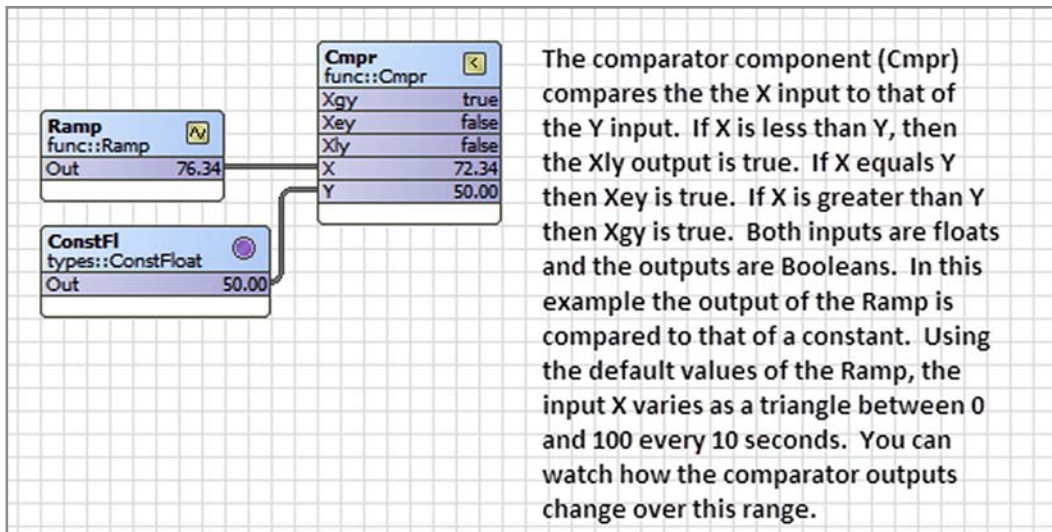


Creating Ramps — A-Stable Multivibrators

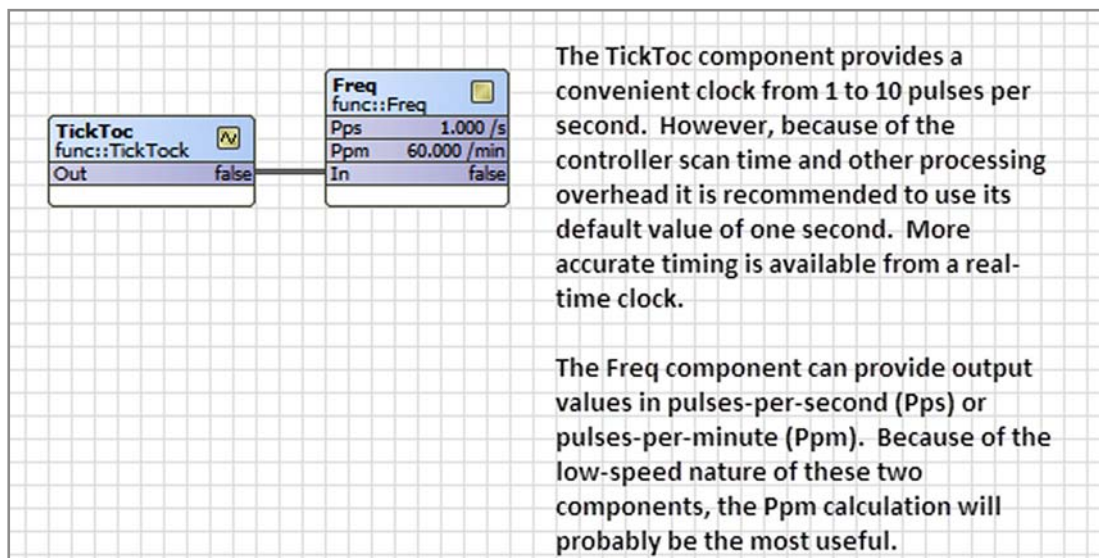


Application Note — Using Sedona 1.2 Components

Comparing Two Floats



A Simple Clock — the TickToc



Application Note — Using Sedona 1.2 Components

Introducing Counters

The diagram illustrates the configuration of two counter components: **Count** and **UpDn**.

Count Component:

- func::Count
- Out: 33
- In: true
- Enable: true
- R: false

UpDn Component:

- func::UpDn
- Out: 33.00
- Ovr: false
- In: true
- Rst: false
- C Dwn: false
- Limit: 100.00
- Hold At Limit: true

Input Components:

- TickToc** (func::TickTock): Out: true
- ConstBo** (types::ConstBool): Out: true
- ConstB1** (types::ConstBool): Out: false
- ConstB2** (types::ConstBool): Out: false
- ConstF1** (types::ConstFloat): Out: 100.00
- ConstB3** (types::ConstBool): Out: true

There are two counters. Count is an up/down counter with an integer output. It must be enabled to count. It can be reset to zero or preset to a positive value as long as the Enable pin is true. The direction pin (Dir) can be connected for programmable up/down counting.

UpDn is an up/down counter with a programmable direction input (C Dwn) which can also be configured. Although counters are inherently integer devices, the output of this component is a float. In this example a limit of 100 has been programmed. Once the limit is hit the overflow bit (Ovr) will be set. If Hold At Limit is true, the counter will not go past 100. If it is false, the counter will continue to count past the limit but the overflow bit will remain set. Resetting the counter returns the component to the start position while clearing the counter and overflow bit.

Operating on Real-World Signals — Hysteresis and Limiting

The diagram illustrates the configuration of two signal processing components: **Hystere** and **Limiter**.

Hystere Component:

- func::Hysteresis
- In: 64.91
- Out: true
- Rising Edge: 60.00
- Falling Edge: 40.00

Limiter Component:

- func::Limiter
- Out: 60.00
- In: 64.91
- Low Lmt: 40.00
- High Lmt: 60.00

Input Components:

- Ramp** (func::Ramp): Out: 65.15
- ConstF1** (types::ConstFloat): Out: 40.00
- ConstF1** (types::ConstFloat): Out: 60.00

The hysteresis component (Hystere) has separate rising-edge and falling-edge trip points when setting a trigger on a float variable. It is ideal for creating a digital event from a real-world analog input. Its output is Boolean.

The Limiter component restricts the range of a float variable by outputting a float that does not exceed the configurable low-limit (Low Lmt) or high-limit (High Lmt). The Limiter only limits the range of its output and does not scale the input float.

Application Note — Using Sedona 1.2 Components

Handling Non-Linear Signals

IRamp

func::IRamp

Out

9

I2F

types::I2F

In

9

Out

9.00

Linear

func::Linearize

Out

91.00

In

9.00

The linearize component (Linear) operates on a float input and creates a piece-wise linear representation of a non-linear input (such as a thermistor) or it can create a non-linear piece-wise representation of a linear input. There is complete flexibility in defining the ten X,Y coordinates along the output curve. The component determines the approximate output between the ten coordinates using linear interpolation.

☐ Meta

Group [1] >>

☐ Out

56.50

☐ In

7.50

☐ X0

0.00

☐ Y0

0.00

☐ X1

1.00

☐ Y1

1.00

☐ X2

2.00

☐ Y2

4.00

☐ X3

3.00

☐ Y3

9.00

☐ X4

4.00

☐ Y4

16.00

☐ X5

5.00

☐ Y5

25.00

☐ X6

6.00

☐ Y6

36.00

☐ X7

7.00

☐ Y7

49.00

☐ X8

8.00

☐ Y8

64.00

☐ X9

9.00

☐ Y9

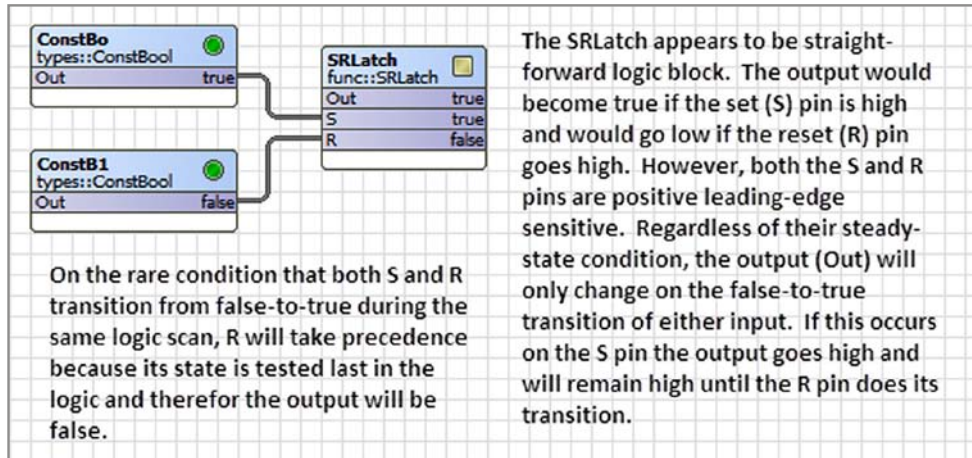
81.00

In this example we will do the reverse of what is commonly done. We will use a linear input and create a non-linear output that approximates the equation $Y=X*X$ over the range of X values from 0 to 9. We need to input corresponding values of Y that obey the desired equation. To make it easy we will use integer values but this is not a restriction. For example, the square of 4 is 16 and the square of 5 is 25. We enter the X values as an independent variable and then the Y value as the dependent variable. We need to be careful that the input does not exceed 9 in this example because we do not define a corresponding value for Y above 9.

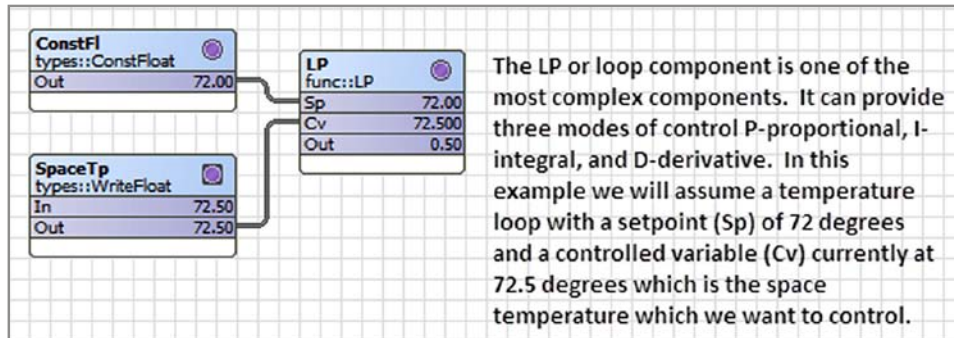
You can test the interpolation by entering a value for X in the In slot assuming no link is connected to the linearize component. This is done here. Notice that the result is 56.50 for an input value of 7.5. The correct value would have been 56.25 which is very close.

Application Note — Using Sedona 1.2 Components

Simple Set-Reset Flip Flop — Bi-Stable Multivibrator



The Loop Component — Basic PID Controller



LP (func::LP)

☐ Meta Group [1] >>

☐ Enable ☒ true

☐ Sp 72.00

☐ Cv 72.500

☐ Out 0.50

☐ Kp 1.000000 [0.000000 - +inf]

☐ Ki 0.000000 /min [0.000000 - +inf]

☐ Kd 0.000000 s [0.000000 - +inf]

☐ Max 100.000000

☐ Min 0.000000

☐ Bias 0.000000

☐ Max Delta 0.000000 [0.000000 - +inf]

☐ Direct ☒ true

☐ Ex Time 1000 ms [0 - max]

Enable must be configured true otherwise there is no control.

Kp is the proportional gain which defaults to 1. Notice that the error signal is Cv-Sp or 0.5. The error multiplied by the proportional gain of 1 yields an output of 0.50. If the Ki and Kd factors are used, their contributions are also multiplied by the proportional gain factor. Ki is the integral gain in units of resets per minute. It is multiplied by the error signal. Kd is the derivative gain in seconds and it is also multiplied by the error signal.

Min and Max are the limits of the output signal. They can be set to any value. Bias can offset the output regardless of the error. Max Delta sets the rate of change of the output within the output limits. This will slow the output swing.

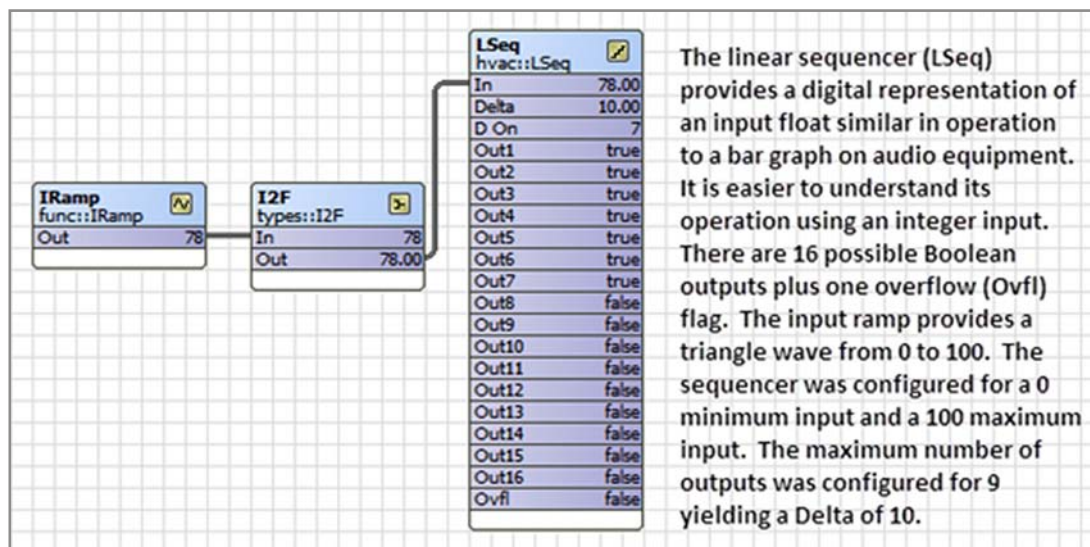
Bias only applies to proportional-only (P) control. When using a PI controller, reset-windup can be minimized by limiting the output range.

For a cooling application set Direct to true. For heating set it for false.

The loop equation is solved each execute time (Ex Time) in milliseconds.

Application Note — Using Sedona 1.2 Components

Linear Sequencer — Bar-Graph Representation of a Float



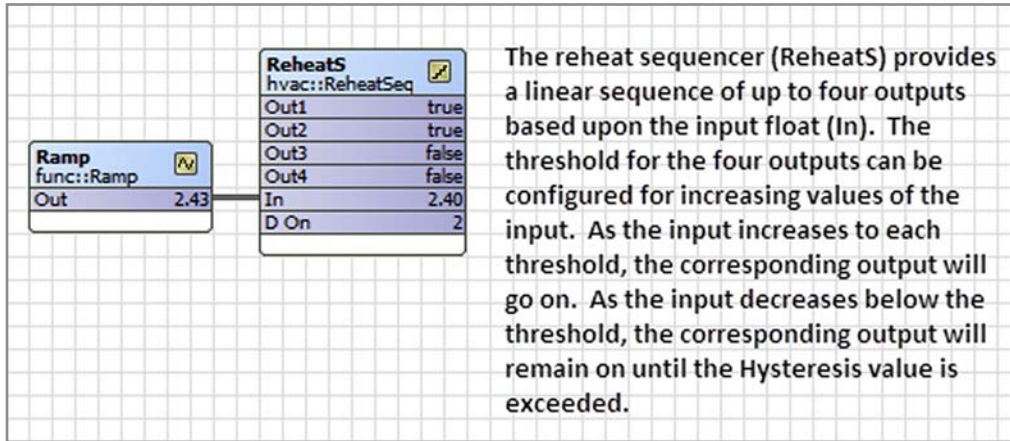
The linear sequencer (LSeq) provides a digital representation of an input float similar in operation to a bar graph on audio equipment. It is easier to understand its operation using an integer input. There are 16 possible Boolean outputs plus one overflow (Ovfl) flag. The input ramp provides a triangle wave from 0 to 100. The sequencer was configured for a 0 minimum input and a 100 maximum input. The maximum number of outputs was configured for 9 yielding a Delta of 10.

Meta		Group [1] >>
<input type="checkbox"/>	In	60.00
<input type="checkbox"/>	In Min	0.00
<input type="checkbox"/>	In Max	100.00
<input type="checkbox"/>	Num Outs	9 [1 - 16]
<input type="checkbox"/>	Delta	10.00
<input type="checkbox"/>	D On	6 [0 - 255]
<input type="checkbox"/>	Out1	<input checked="" type="radio"/> true
<input type="checkbox"/>	Out2	<input checked="" type="radio"/> true
<input type="checkbox"/>	Out3	<input checked="" type="radio"/> true
<input type="checkbox"/>	Out4	<input checked="" type="radio"/> true
<input type="checkbox"/>	Out5	<input checked="" type="radio"/> true
<input type="checkbox"/>	Out6	<input checked="" type="radio"/> true
<input type="checkbox"/>	Out7	<input type="radio"/> false
<input type="checkbox"/>	Out8	<input type="radio"/> false
<input type="checkbox"/>	Out9	<input type="radio"/> false
<input type="checkbox"/>	Out10	<input type="radio"/> false
<input type="checkbox"/>	Out11	<input type="radio"/> false
<input type="checkbox"/>	Out12	<input type="radio"/> false
<input type="checkbox"/>	Out13	<input type="radio"/> false
<input type="checkbox"/>	Out14	<input type="radio"/> false
<input type="checkbox"/>	Out15	<input type="radio"/> false
<input type="checkbox"/>	Out16	<input type="radio"/> false
<input type="checkbox"/>	Ovfl	<input type="radio"/> false

The range of the linear sequencer is configured using In Min at the low-end and In Max at the high-end. Selecting the number of outputs (Num Outs) determines the difference (Delta) between successive outputs turning on. In this case the range is 100 and the number of desired outputs is 9. Divide 100 by Num Outs + 1 and you will get a Delta of 10.

You will notice that the input (In) is at 60 and D On is indicating that six outputs are on. With an input between 0-9, there are no outputs on but once you hit a decade such as 10, 20 on up to 90, successive outputs will come on. At the maximum of 100, 9 lights will be on. If the input exceeds the maximum intended, the overflow flag will set but the number of outputs will remain as specified by Num Outs.

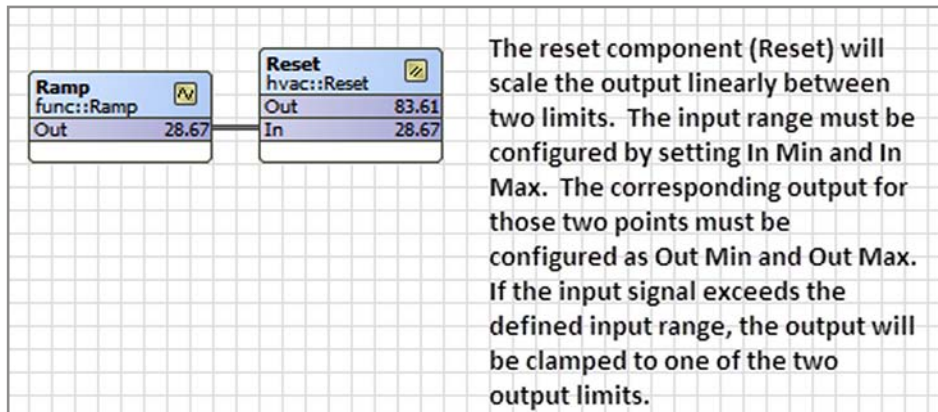
Reheat Sequencer — Four Staged Outputs from a Float Input



ReheatS (hvac::ReheatSeq)		
<input type="checkbox"/> Meta	Group [1] >>	
<input type="checkbox"/> Out1	<input checked="" type="radio"/> true	Enable must to true otherwise the outputs to be false.
<input type="checkbox"/> Out2	<input checked="" type="radio"/> true	
<input type="checkbox"/> Out3	<input checked="" type="radio"/> true	There are four possible threshold settings corresponding to four outputs. As the input signal increases to each threshold its corresponding output goes on and stays on until the input drops below the threshold plus the value of the hysteresis.
<input type="checkbox"/> Out4	<input type="radio"/> false	
<input type="checkbox"/> In	2.93	
<input type="checkbox"/> Enable	<input checked="" type="radio"/> true ▼	
<input type="checkbox"/> D On	3 [0 - 255]	
<input type="checkbox"/> Hysteresis	0.25	
<input type="checkbox"/> Threshold1	1.00	The input signal is decreasing but it has not exceeded the amount of the threshold so output 3 (Out3) remains set. Once the signal is below 2.75, output 3 will go off.
<input type="checkbox"/> Threshold2	2.00	
<input type="checkbox"/> Threshold3	3.00	
<input type="checkbox"/> Threshold4	4.00	

Application Note — Using Sedona 1.2 Components

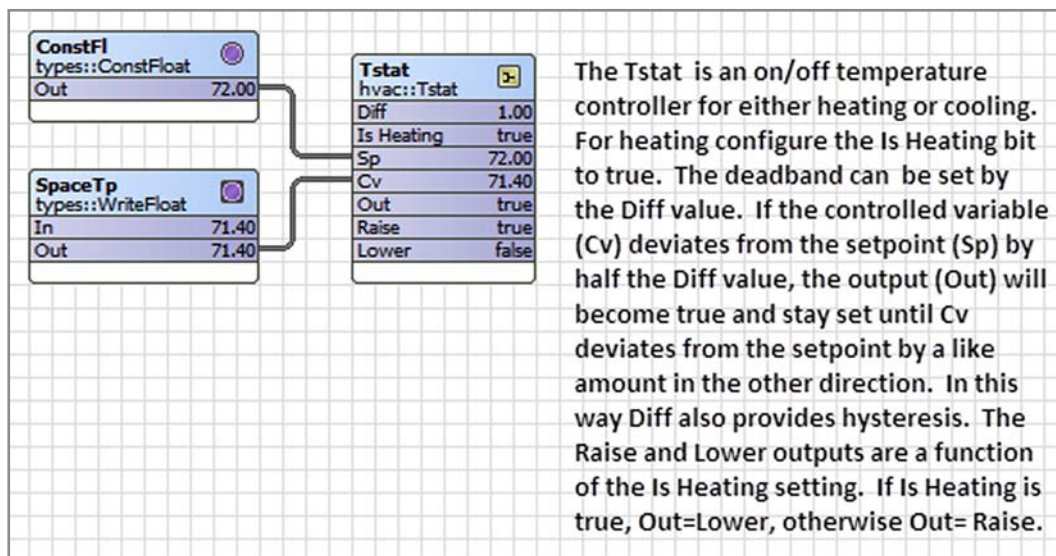
Reset — Scaling a Float Input between Two Limits



Reset (hvac::Reset)	
<input type="checkbox"/> Meta	Group [1] >>
<input type="checkbox"/> Out	81.22
<input type="checkbox"/> In	27.34
<input type="checkbox"/> In Min	0.00
<input type="checkbox"/> In Max	100.00
<input type="checkbox"/> Out Min	32.00
<input type="checkbox"/> Out Max	212.00

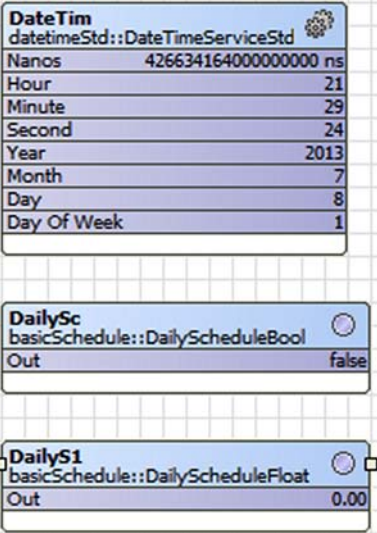
In this example we are converting degrees Celsius to degrees Fahrenheit within the 0-100 degree Celsius range. Therefore we set Out Min and Out Max to the corresponding Fahrenheit values. All Celsius input values between these two limits will be interpolated thereby providing the correct Fahrenheit values.

Tstat — Basic On/Off Temperature Controller



Application Note — Using Sedona 1.2 Components

Real-Time Clock and Scheduling



The **DateTim** component (datetimeStd::DateTimeServiceStd) displays the following values:

Property	Value
Nanos	426634164000000000 ns
Hour	21
Minute	29
Second	24
Year	2013
Month	7
Day	8
Day Of Week	1

The **DailySc** component (basicSchedule::DailyScheduleBool) has an output **Out** set to **false**.

The **DailyS1** component (basicSchedule::DailyScheduleFloat) has an output **Out** set to **0.00**.

The **DateTim** component provides real-time information. There is no need to place it on the wiresheet. However, if you need specific information from the component for driving logic, you can connect to the various integer outputs such as Hour, Minute and Second.

There are two schedule components which have different output types. One is for Boolean and the other for float. There is no need to connect the **DateTim** component to either of the schedulers. Each scheduler can handle two events over the 24 hour period by configuring the time and duration of each event. The output of each schedule will change with each event. If more events or more outputs are needed, multiple schedulers can be placed on the wiresheet.

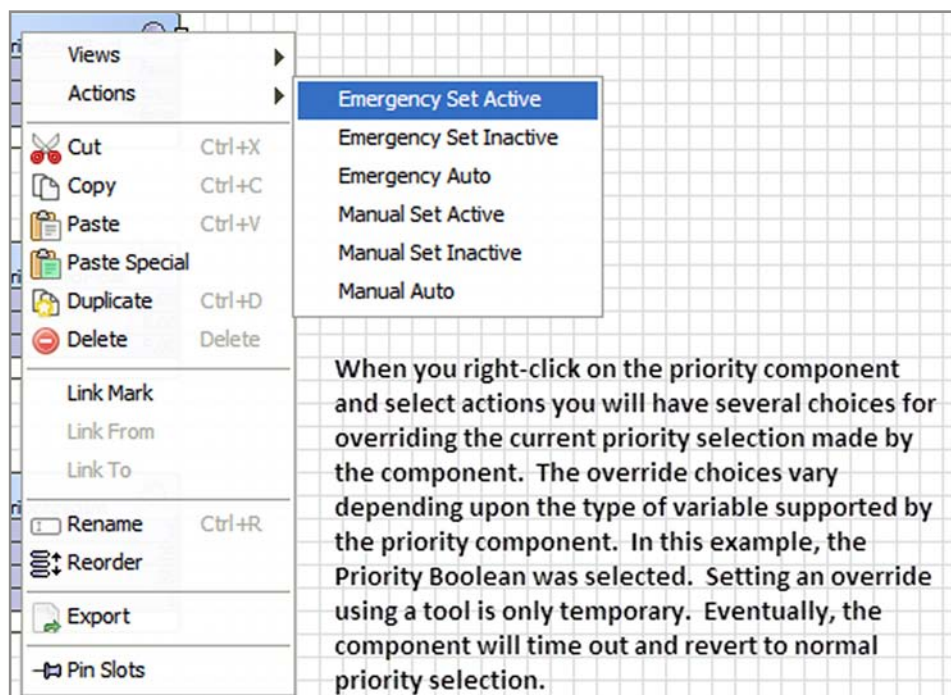
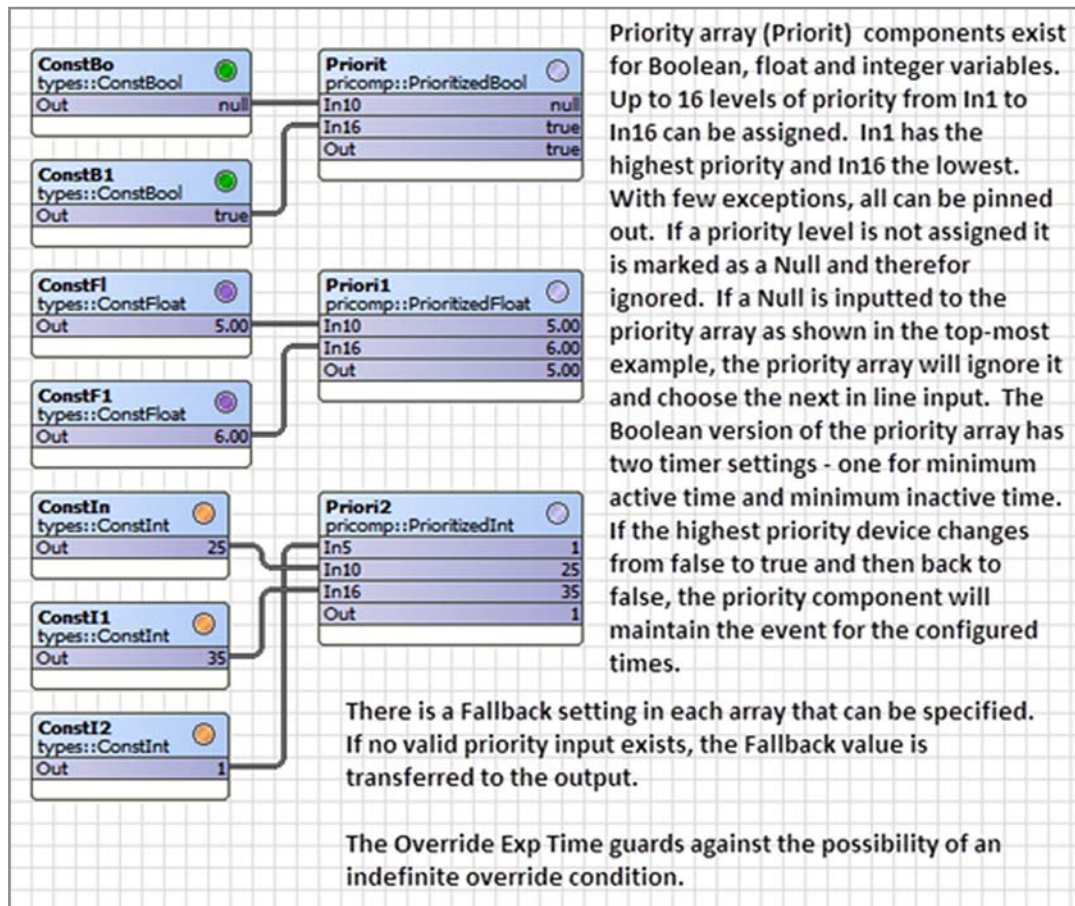
DailyS1 (basicSchedule::DailyScheduleFloat)

Property	Value	Range
Meta	Group [1] >>	
Start1	12:00 AM	
Dur1	00000h 00m	[0ms - 1day]
Start2	12:00 AM	
Dur2	00000h 00m	[0ms - 1day]
Val1	0.00	
Val2	0.00	
Def Val	0.00	
Out	0.00	

Configuration of the two scheduler components is similar. For the float version, Val1 and Val2 need to be specified along with the start times (Start1 and Start2) and the durations (Dur1 and Dur2). The output (Out) will assert either Val1 or Val2 during the scheduled times. If neither are programmed, the Def Val should be configured.

Application Note — Using Sedona 1.2 Components

Priority Arrays



United States

Contemporary Control Systems, Inc.

2431 Curtiss Street
Downers Grove, IL 60515
USA

Tel: +1 630 963 7070
Fax: +1 630 963 0109

info@ccontrols.com
www.ccontrols.com

China

Contemporary Controls (Suzhou) Co. Ltd

11 Huojia Road
Science & Technology
Industrial Park
New District, Suzhou
PR China 215009

Tel: +86 512 68095866
Fax: +86 512 68093760

info@ccontrols.com.cn
www.ccontrols.asia

United Kingdom

Contemporary Controls Ltd

14 Bow Court
Fletchworth Gate
Coventry CV5 6SP
United Kingdom

Tel: +44 (0)24 7641 3786
Fax: +44 (0)24 7641 3923

ccl.info@ccontrols.com
www.ccontrols.eu

Germany

Contemporary Controls GmbH

Fuggerstraße 1 B
04158 Leipzig
Germany

Tel: +49 341 520359 0
Fax: +49 341 520359 16

ccg.info@ccontrols.com
www.ccontrols.eu